

EF1SRP-01U Supplement (M16C/20, 21 Series Edition)

First edition issued March, 1999

1. General Description

The supplement contains information required for reading, writing and clearing data to/from Mitsubishi Electric M16C/20, 21 Series MCU with built-in flash memory. The supplement also contains a description of command operation for the various functions of the M16C/20, 21 Series.

2. Operating Environment

Use the MCUs mentioned in this supplement in an environment as follows.

[EFP-I]

Monitor Version: Ver.3.00.03 or later

[Control Software]

WinEFP Version: Ver.1.02.00 or later

EFP MCU.TBL Version: Ver.2.02.00 or later

SRP MCU.TBL Version: Ver.1.02.02 or later

3. Individual Writing Specifications

- 4-wire serial writing
- ID collation function
- Block erase (only M16C/21 Series)
- Boot area output function
- Page program (256 bytes)

4. Pin Connection

Table 4.1 lists the connection of target connection cable pin of the M16C/20, 21 Series.

Table 4.1: Connection of the Target Connection Cable Pin

Pin No. (EF1SRP-01U side)	Target End Wire Color	Signal	4-Wire Cable Pin No.	MCU Connection Pin for Serial Writing
1, 2	Orange/red dotted 1, Orange/black dotted 1	GND	1	Connects to VSS pin *4
3	Gray/red dotted 1	T_VPP	4	Connects to CNVSS pin
4	Gray/black dotted 1	T_VDD	5	Connects to VCC pin (VCC is used for user power source) *1
8	White/black dotted 1	T_PGM/OE/MD	8	Unconnected
9	Yellow/red dotted 1	T_SCLK	6	Connects to CLK0 pin
10	Yellow/black dotted 1	T_TXD	7	Connects to RXD0 pin
11	Pink/red dotted 1	T_RXD	2	Connects to TXD0 pin
12	Pink/black dotted 1	T_BUSY	3	*2
14	Orange/black dotted 2	T_RESET	9	Connects to RESET pin *3
15, 16	Gray/red dotted 2, Gray/black dotted 2	GND	10	Connects to VSS pin *4

*1. Supply VCC from user side to match source voltage of output buffer used on EFP-I side with user side source voltage (VCC).

*2. For M16C/20 Series, connect to CLKS, and for M16C/21, connect to CTS0/RTS0.

*3. Reset cancel is not carried out following write verify. To execute MCU, you should therefore unplug the target connection cable.

*4. The signal GND has 4 pins (No. 1, 2, 15 and 16) of EF1SRP-01U side connector. When connecting to the target board, there is no problem for connecting only one pin, but it is recommended to connect more than 2 pins.

*5. Connect the MCU's Xin and Xout terminals to the oscillator circuit.

(1) An example of target MCU peripheral circuit when using the M16C/20, 21 Series is shown in Fig. 4.1.

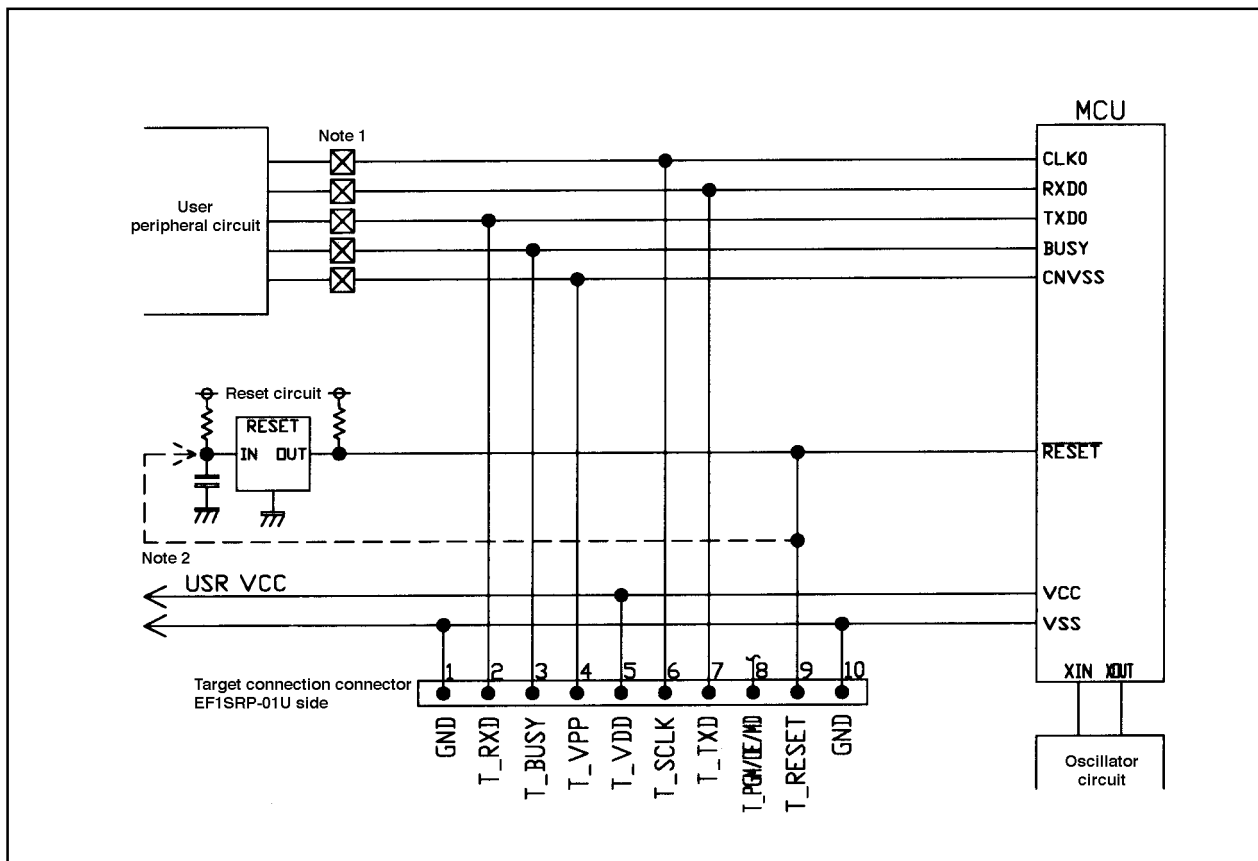


Fig. 4.1: Target MCU Peripheral Circuit Example

Notes

1. If the user peripheral circuit is an output circuit, you should disconnect by jumper to avoid output collision when serial writing.
2. EFP-I side reset output is an open collector, therefore connect directly to the $\overline{\text{RESET}}$ pin for open collector output. A pull-up resistor however must be connected. If the reset circuit is CMOS output, do as described in note 1, or connect the EFP-I side T_RESET signal to reset circuit input. Make reset delay within 30ms.

(2) An example of a collision prevention circuit when user peripheral circuit outputs is shown in Fig. 4.2.

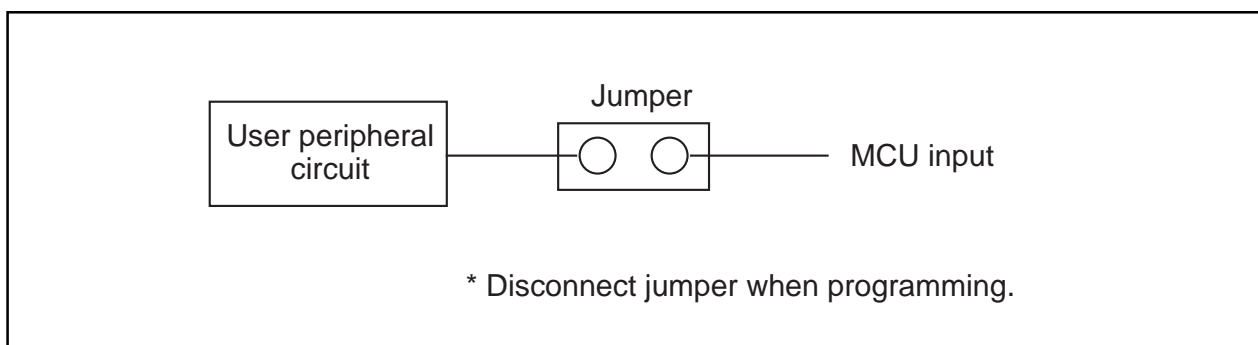


Fig. 4.2: Collision Prevention Circuit Using Jumper

5. ID Code Area

ID code area is provided for FFFDFH, FFFE3H, FFFE3H, FFFE3H, FFFE3H, FFFF3H, FFFF7H and FFFFBH of the internal flash memory of M16C/20, 21 Series MCUs.

ID data size is fixed at 7 bytes. Set the ID code in each ID area one byte at a time.

MCUs for which an ID code has been written in the ID code area carry out ID code collation and the MCU's internal flash memory cannot be read, written in or cleared unless the ID code matches.

* Does not include when the ID code area is blank.

ID code area configuration is shown in Fig. 5.1.

FFFDH	ID code (1st byte)
•	•
FFFE3H	ID code (2nd byte)
•	•
FFFE3H	ID code (3rd byte)
•	•
FFFE3H	ID code (4th byte)
•	•
FFFF3H	ID code (5th byte)
•	•
FFFF7H	ID code (6th byte)
•	•
FFFBH	ID code (7th byte)

Fig. 5.1: ID Code Area Configuration

6. ID Collation Function

The ID code collation is carried out by inputting the ID code etc. for the ID collation parameter of the WinEFP environment setting dialog. Be sure to carry out the ID code collation if an ID has been written in the ID code area of the target MCU.

The commands of [Device] of the WinEFP window menu cannot be used if the ID code does not match.

* If the ID code area is blank, the ID collation is not carried out even if you input the ID code.

The ID collation parameter configuration is shown in Fig. 6.1.

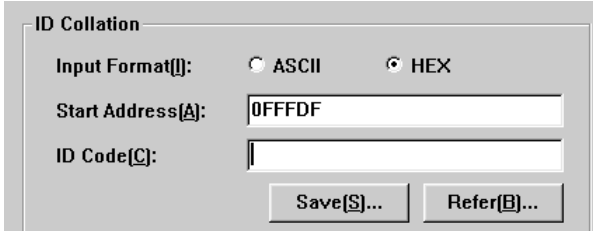
The image shows a dialog box titled "ID Collation". It contains three input fields: "Input Format(I):" with radio buttons for "ASCII" and "HEX" (HEX is selected), "Start Address(A):" with a text box containing "0FFDF", and "ID Code(C):" with an empty text box. At the bottom right, there are two buttons: "Save(S)..." and "Refer(B)...".

Fig. 6.1: ID Collation Parameter Configuration

(1) Input format

The ID code input format is specified as ASCII or HEX.

(2) Top address

Specifies the top address of ID code area. The top address is fixed at FFFDFH for the M16C/20, 21 Series.

(3) ID code

Input the ID code. The number of bytes for ID code input is fixed at 7 bytes.

(4) Save button

Saves input ID collation parameters in a file. When the save button is clicked, the file section dialog appears, so input the file name of your choice.

(5) Refer button

Reads the file in which ID collation parameters are saved and sets parameters according to the contents of the file. When the refer button is clicked, the file section dialog appears, so select the file of your choice.

* An example of the operating procedure for ID collation is shown below. The ID code area of the target MCU's internal flash memory is set as listed in Table 6.1.

Table 6.1: ID Code Setting

ID Code Area Address	ID Data
FFDFH	53H
FFFE3H	55H
FFFE7H	49H
FFFEFH	53H
FFFF3H	45H
FFFF7H	49H
FFFFBH	2EH

- (1) Check if FFFDFH is set for the top address parameter. If the device type parameter is set to "M3020xFx" or "M3021xFx", FFFDFH is automatically set as the top address of the code collation parameter. The ID code will not match if an address other than FFFDFH is set.
- (2) Set ASCII or HEX for the input format parameter, and input a 7-byte ID code for the ID code parameter. An example of ID code parameter setting is given below.

For ASCII:

ID Code: SUISEI.

For HEX:

ID Code: 53 55 49 53 45 49 2E
S U I S E I .

7. Erase Command

The erase type parameter contained in the erase command enables you to erase by block or erase all blocks. The erase command parameter input dialog is shown in Fig. 7.1.

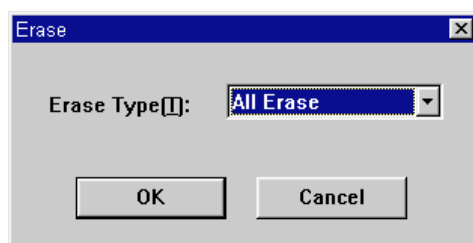


Fig. 7.1: Erase Command Parameter Input Dialog

(1) Erase type (only M16C/21 Series)

All Erase and block address area (xxxxxxH - xxxxxxH) are displayed in the drop-down list to the right of the erase type parameter display field (displayed by clicking the arrow pointing downward with the mouse). Select the block erase method.

As the block erase function is not controlled by M16C/20 Series MCUs, it is impossible to set the parameter.

(2) OK button

Executes the erase command.

(3) Cancel button

Cancels the command.

8. Boot Read Command

Reads the contents of the boot area, and then writes in the EFP-I's internal RAM. Specify DF000H to DFDFFH for the start and end address parameter.

Unfixed data will be read if address outside the start and end address area is specified.

9. Parameter Input by Device Command

M16C/20, 21 Series serial writing MCUs read and write data by the page. Because one page of data is 256 bytes, the input format for the start and end addresses is set as follows.

A parameter error results if addresses outside the page unit are input for the start and end addresses.

Input format:

Start address xxxx00H

End address xxxxFFH

10. Auto Offset Address of EFP-I's Internal RAM

Capacity of the EFP-I's internal RAM is 512K bytes. Thus the area from 0H to 7FFFFH can be used under ordinary circumstances. The address area of the M16C/20, 21 Series internal flash memory is exceeding the address area, so the HEX offset address of 80000H is automatically set for the EFP-I's internal RAM. When 80000H is set for the HEX offset, the 0H at the top of the EFP-I's internal RAM changes to 80000H and the end address changes to FFFFFH.

Specify 80000H to FFFFFH for the start and end address parameter in the [Edit] command of the WinEFP window menu.